

LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S
INFORMATION DISCLOSURE STATEMENT

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APPLICANT:
David K. Parker et al.FILING DATE:
March 30, 2004GROUP:
2661 2419

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
/D.H./	AA 6,295,299	9/25/2001	Haddock, et al.			
/D.H./	AB 6,034,957	3/7/2000	Haddock, et al.			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES NO
	None					X
						X

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

/D.H./	AC	Van Ess, D., <i>A Circular FIFO, PSOC Style, Cypress Microsystems, Application Note AN2036</i> , Rev. B, pp. 1-5, June 21, 2002.
/D.H./	AD	Network Working Group, <i>RFC 1071 - Computing the Internet Checksum</i> , September 1988, available at: http://www.faqs.org/rfcs/rfc1071.html
/D.H./	AE	Network Working Group, <i>RFC 1141 - Incremental Updating of the Internet Checksum</i> , January 1990, available at: http://www.faqs.org/rfcs/rfc1141.html
/D.H./	AF	Network Working Group, <i>RFC 1624 - Computation of the Internet Checksum via Incremental Update</i> , May 1994, available at: http://www.faqs.org/rfcs/rfc1624.html
/D.H./	AG	Netlogic Microsystems, Product Brief NSE5000GLQ, Copyright 2004, available at: http://www.netlogicmicro.com/datasheets/nse5000glq.html
/D.H./	AH	Zhang, Z., <i>Recovery of Memory and Process in DSM Systems: HA Issue #1</i> , Hewlett-Packard Co. HPL-2001-76, March 30, 2001.

EXAMINER: /Duc Ho/ (10/10/2008)

DATE CONSIDERED:

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